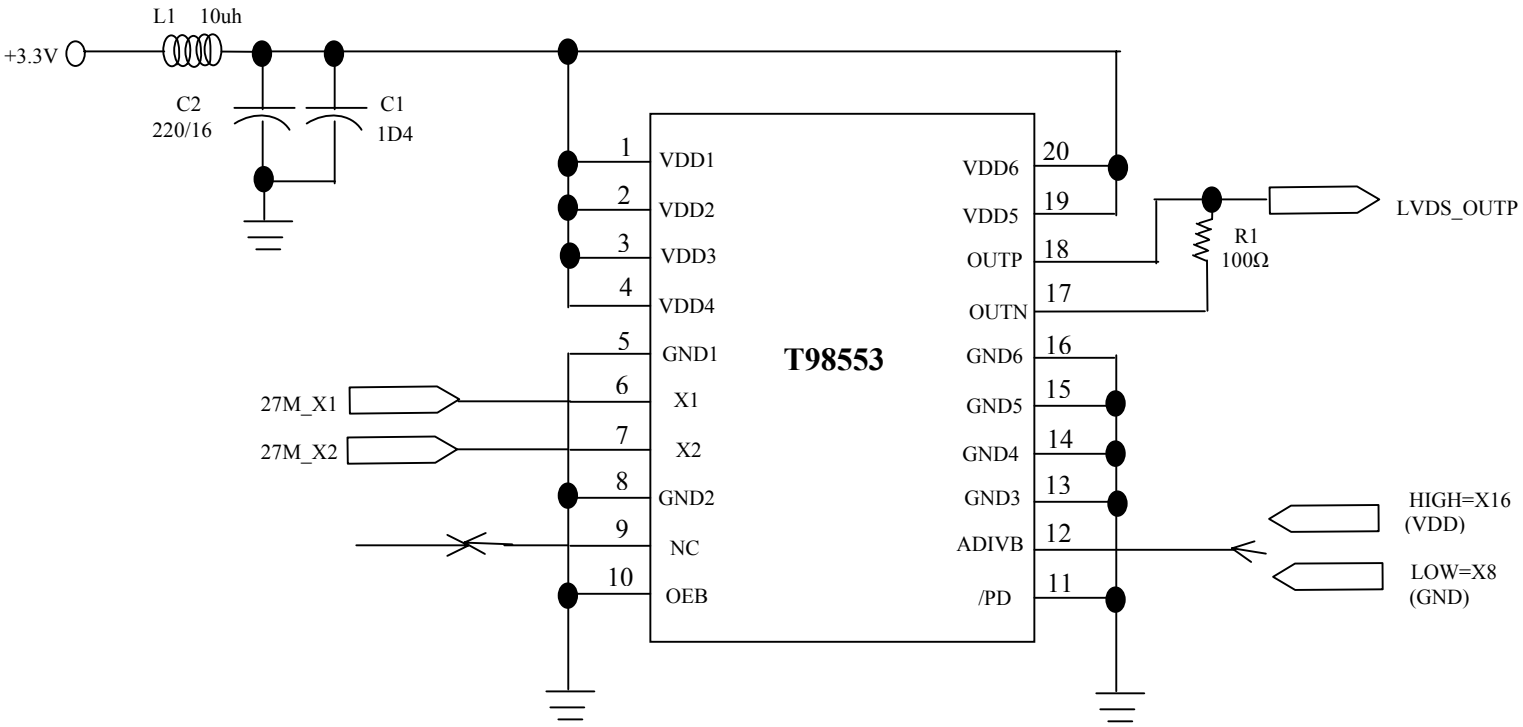
T98553 IN A SINGLE-ENDED OUTPUT CONFIGURATION

While the T98553 has a differential LVDS output and was designed for high speed, high frequency systems, it can also be used safely in single-ended output applications. Either the OUTN (pin 17) or OUTP (pin 18) can be used as a single-ended clock signal. **However, in this type of application, it is still necessary to properly terminate the LVDS outputs to ensure proper operation.** The schematic shown below shows how to properly terminate these outputs:



Note: For single-ended operation, if the termination resistor, R1 (100Ω), is not installed, the clocks will be indeterminate and the device will not function properly. For true differential operation, if the LVDS output pins are properly connected to an LVDS receiver, they would then be properly terminated and R1 would not be required. Alternately, OUTN (pin 17) may also be used in a single-ended application, instead of OUTP (pin 18).

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